

# **Projects plans & deliverables:**

The current challenge for the project is to provide to industrial domain like broadcasting, transport, military and security, the access to cost effective embedded computing systems, necessary for the global performance of their products. It will

### constitute the French pôle able to design embedded HPC platform of tomorrow which competes with the best international initiatives.

# **Deliverables** :

Concepts and Specification of the platform

- Simulator of the architecture allowing the end users to evaluate the performances on their own applications
- •A preliminary version of the software development framework allowing to efficiently develop the application on this massively parallel architecture







# **Status / achievements:**

Kick off in February 2007

 Application requirements analysis and state of the art •Specifications of Architecture, Virtual Machine and Framewor GPP Calcul

Simulator infrastructure development

•Web site : http://teraops-emb.ief.u-psud.fr **Coordinator:** 

Thales Research & Technology **Durations: 2 years** 

**Global budget: 9648 k€** 

## **Funding:** k€

DGC/FCE	1047
CRIF	1431
départements	1091
Total	3569

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